**North South University**

Department of Computer Science and Engineering

Quiz-5, Section – 4, Spring-2017

Course No: **CSE 231** Course Title: **Digital Logic Design**

Time:20 min Full Marks: 10

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| 1. | Design a 2 bit counter. It will have one manual input. The counter behaves such that when input=1, the counter will count even number and repeat. If the input =0, the counter counts odd number. You have to provide the following things.  a. Tabulate the state table  b. Draw state diagram  c. Draw the logic diagram of the circuit | 10 |
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